



International Conference on Micro/Nanoelectronics Devices, Circuits and Systems (MNDCS-2021) 30-31 Jan 2021



About the Conference: The *Department of Electronics and Communication Engineering, National Institute of Technology Silchar, Assam, India* in association with *IEEE ED NIT Silchar Student Branch Chapter* and *Institute Innovation Cell (IIC), NIT Silchar* organises *International Virtual Conference on MNDCS-2021* (<http://mndcs.nits.ac.in/>) during **30-31 Jan 2021**. The objective of this conference is to promote advanced research and developments in the areas of Microelectronics, Nanoelectronics, Semiconductor Devices, VLSI Circuits and Systems. The conference aims to foster the theme through keynotes, invited talks, and oral presentations of research articles in the most relevant areas allied to the theme of the conference. It'll also create an international platform to share research ideas, scientific thoughts and research problems among academicians, researchers, technologists and scientists.

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- Dr. Vilas H Gaidhane, BITS Pilani, Dubai Campus
- Dr. Ujjal Chakraborty, NIT Silchar
- Dr. Taimor Khan, NIT Silchar

Keynote/Invited Speakers



Prof. C. Jagadish
ANU, Australia



Prof. Samar Saha
Prosperit Tech, US



Dr. Zoran Jaksic
Uni of Belgrade, Serbia

Nanowires for Optoelectronics



Prof. Lan Fu
ANU, Australia



Dr. Ajay Agrawal
CEERI, India



Prof. H. P. T Nguyen
NJIT, US

Nanowire LED

Call for Papers: Topics of Interest Include (but are not limited to)

Micro/Nanoelectronic Devices: Device Physics, Quantum Electronics, Quantum Transport, Compact Device Modeling, LED, MOSFET, MESFET, FinFET, TFET, Beyond CMOS, III-V HEMT, Ga₂O₃ HEMT, SET, UWB Semiconductor Materials and Devices, 2D Materials: Graphene, Spintronics, Carbon Nanotubes, Nanotechnology: Nanowires, Nanostructures, Flexible Electronics, High Efficiency Solar Cells, Novel Photovoltaic Concepts, Energy Storage, Advanced materials, Advanced Electronic Devices etc.

Micro/Nanoelectronic Circuits: Analog VLSI Circuits, Digital VLSI Circuits, Mixed-mode VLSI Circuits, System on Chip (SoC), Bioelectronics Circuits, Circuit Optimization Techniques, Reconfigurable Circuits, HDL based FPGA Design, Testing and Verification, Packaging, Reliability, DSP based VLSI.

Micro/Nano Systems: MEMS/NEMS, Sensors, Actuators, Nanogenerator, Energy Harvester, Micromachining, Microfluidics, Lab-on-Chip, Health care systems, Embedded System Design, Biomedical Systems, IoT and Smart Systems, Robotics and Mechatronics, MEMS Packaging, Reliability, Semiconductor Memories etc.

Important Dates:

- Full Paper Submission Extended: Dec 15, 2020 (Springer LNEE Proceedings format)
- Notification of Acceptance: Dec 30, 2020
- Deadline for Accepted Revised Full Paper: Jan 15, 2021
- Format: <https://www.springer.com/us/authors-editors/conference-proceedings>

Paper Submission and Publication:

The submission Web page for MNDCS-2021 is

<https://easychair.org/conferences/?conf=mndcs2021>.

The peer-reviewed and selected papers of the conference presentation will be published in Scopus indexed *Springer Conference Proceedings in their prestigious "Lecture Notes in Electrical Engineering (LNEE)" series* (<https://www.springer.com/series/7818>). Springer will conduct quality checks on the accepted papers and only papers that pass these checks will be published.

Contact Details:

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Authors	Indian Early Bird (By 10 Jan 2021)	Standard	Foreign Early Bird (By 10 Jan 2021)	Standard
Students	3000 INR	3500 INR	100 USD	150 USD
Faculty/Scientists	4000 INR	4500 INR	200 USD	250 USD
Industry	5000 INR	5500 INR	300 USD	350 USD

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Keynote/Invited Speakers



Prof. Ilya Sychugov, KTH, Sweden

Talk: Silicon Nanocrystals: fabrication, physical properties and applications



Prof. Yong Shi

Stevens Inst. of Tech, NJ, USA

Talk: Piezoelectric Nanogenerator for Medical Devices

Journal Publications:

The selected and extended papers will be recommended for publication in

- **International Journal of Reconfigurable and Embedded Systems** (Scopus)
- **Facta Universitatis: Series Electronics and Energetics** (ESCI)

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